ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

EFS ID:

[CLOCK SIGNAL AMPLIFYING METHOD AND DRIVING STAGE FOR LCD DRIVING CIRCUIT]

Submision Type: Utility Patent Filing

Application Number:

10/708178

55504

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First Named Applicant: Jian-Shen Yu Attorney Docket Number: 10929-US-PA

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File Listing:

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		9956 2004-02-13
us-assignment	10929assignment2.tif 10929-usrequ.xml	9956 2004-02-13 2149 2004-02-13
us-request	· ·	
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us-request	us-request.xsl	33300 2004-02-13
us-fee-sheet	10929-usfees.xml	1700 2004-02-13
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